Form PTO-1449 US Dept. of Commerce (REV. 8-83) PATENT & TRADEMARK OFFICE		ATTY DO 100629.0	Y DOCKET NO. 29.03		RULE 53(b) DIVISION OF APPLICATION NO. 10/267,834			
INFORMATION DISCLOSURE STATEMENT								
(Use several sheets if necessary)			APPLICANT(S) Takuroh OZAWA et al.					
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	U.S.	PATI	ENT DOCU	MENTS				
EXAMINER INITIAL	DOCUMENT NUMBER		DATE	NAME			CLASS	SUB CLASS
J.L.	5,714,838	2-19	998	HAIGHT et al.	··			
	6,072,450	6-2000		YAMADA et al.				
	5,587,329	12-9	96	HSEUH et al.				
	5,828,429	10-9	98	TAKEMURA				-
	6,023,307	02-0	00	TAKEMURA				
	5,670,792	09-9	97	UTSUGI et al.				
	6,011,529	01-0	00	IKEDA				
	5,995,073	11-9	99	ISAMI et al.				
	6,243,069	06-01		OGAWA et al.				
J.X.	5,869,929	02-9	99	EIDA et al.				
FOREIGN PATENT DOCUMENTS								
	DOCUMENT NUMBER]	DATE	COUNTR	Y		CLASS	SUB CLASS
J.L.	EP-A1-0 653 741	05-9	>5	Europe				
	JP-A-8-129358	05-9	96	Japan				
	JP 8-227276	09-9	96 Japan					<u></u>
	JP 08 241057 A	09-9	96	Japan				
	EP 0112 700 A	07-8	34	Japan				
	WO 96 06456 A	02-9	96	WIPO				
	JP 08 129360 A	05-9	96	Japan				
	EP 0717 439 A	06-9	96	Europe				
J.7.	JP 08-241057 (translation only)	9-96	5	Japan				
	OTHER DOCUMENTS (Inc	ludin	g Author, T	itle, Date, Pertinent Pages	, etc.)			
J.L.	Alan G. Lewis, et al., Physical Mechan 13.4.1-13.4.4	isms l	For Short C	hannel Effects In Polysilic	on Thin File	m Tran	sistors, IEEE	E, 1989, pgs.
J. L.	Alan G. Lewis et al., Polysilicon TFT C Vol. 27, No. 12, pgs. 1833-1842.	Circuit	Design and	Performance, IEEE Journ	nal of Solid-	State C	ircuits, Dece	mber 1992,
	ean degerance				DATE CO	6-9	1-05	
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not conformance and not considered. Include copy of this form with next communication to applicant.							on if not in	

Date: July 24, 2003

Sheet 1 of 1

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J.L.	1	5,786,796	07/	1998	TAKAYAMA et al.			345	76		
J.L.	2	5,986,632	11/	1999	TAKAYAMA et al.			345	99		
J.L.	3	5,463,279	10/1995		KHORMAEI			315	169.3		
											
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		DOCUMENT NUMBER	DATE		COUNTR	COUNTRY		CLASS	CLASS		
J.L.	4	EP 0 653 741 Al	05/1995		EUROPE						
J.L.	5	JP 08-241057 (w/ English abstract)	09	/1996	JAPAN						
J.1.	6	WO 96/06456	02	/1996	WIPO						
T.L.	7	EP 0 112 700	07	7/1984	EUROPE						
7.6.	8	JP 08-129360 (w/ English abstract)	05	5/1996	JAPAN						
J.L.	9	EP 0 717 439 A2		5/1996	EUROPE			<u>L</u>			
					, Title, Date, Pertinent Pages						
J.L.	10	Lewis A. G. et al.: "Polysilicon TFT (York, US, Vol. 27, No. 12, 1 Decemb	Circu er 19	it Design ar 992	nd Performance", IEEE Journ	nal of Sol	id-State (Circuits, IEI	EE Inc. New		
J.L.	11	Lewis A. G. et al.: "Physical Mechan the International Electron Devices Me	isms eting	for Short C 3. Washing	Channel Effects in Polysilicon ton, Dec. 3-6, 1989, New Yo	Thin Filork, IEEE	m Transi , US	stors", Proc	eedings of		
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J.L.	JP-A-9-106887 (w/ Eng. Abstract)	+	1997	JAPAN				
	JP-A-10-12377 (w/ Eng. Abstract)	├	1998	JAPAN				
J.L.	EP 0 880 303 A1	 	1998	EUROPE				
J.L.	EP 0 849 721 A2 EP 0 961 525 A1	 	1998 1999	EUROPE		-		
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Date: November 6, 2003

Sheet 1 of 1

Form PTO-1449 US Dept. of Commerce (REV. 8-83) PATENT & TRADEMARK OFFICE			ATTY DOCKET NO. 100629.03			APPLICATION NO. 10/625,617				
INFORMATION DISCLOSURE STATEMENT										
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J.L.	1	JP 04-161984 (Eng. Abstr Only)	06/0	5/1992	JAPAN	IPAN		A		
J.L. J.L. J.L.	2	JP 08-129359(Eng.Trans&Abstr)	05/2	1/1996	JAPAN			Α		
J-L.	3	JP 09-016123(Eng.Trans&Abstr)	01/1	7/1997	JAPAN			A		
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Form PTO-1449 (REV. 8-83) PATENT & TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT				ATTY D 100629.0	OCKET NO. 13		APPLICATION NO. 10/625,617			
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Date: February 9, 2005

Examiner: